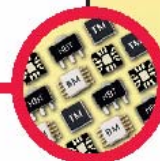




GaAs MMIC Power Amplifier



AM153040WM-BM-R

AM153040WM-FM-R

December 2009

DESCRIPTION

AMCOM's AM153040WM-BM-R and AM153040WM-FM-R are part of the GaAs HiFET MMIC power amplifier series. It is a 2-stage GaAs HIFET PHEMT MMIC power amplifier. It is fully matched to 50-ohm at both input and output, covering 1.4 to 3.0 GHz. The MMIC has 18dB gain and 38dBm output power at 12V. The BM package RF and DC leads are coplanar with the bottom level of the package, which serves as ground, to facilitate low-cost SMT assembly to the PC board. Because of high DC power dissipation, we strongly recommend to mount this device directly on a metal heat sink. The FM package is the BM package mounted on a copper flange carrier. There are two screw holes on the flange to facilitate screwing on to a metal heat sink. Both MMICs are RoHS compliant.

FEATURES

- Frequency applications from 1.4 to 3.0 GHz
- High output power, P1dB = 37dBm
- Gain = 18 dB
- Input & Output matched from 1.4GHz to 3.0GHz

APPLICATIONS

- PCS Base Station
- GPS Applications
- MMDS
- WLAN Repeaters
- 10V – 13V Applications

TYPICAL PERFORMANCE ON A TEST BOARD*

Performance at $V_{dd} = +12V$, $V_{gs} = -0.90V^{**}$, $I_{dq} = 1300mA$, $T_a = 25^{\circ}C$

Parameters	Minimum	Typical	Maximum
Frequency	1.8 – 3.0 GHz	1.4 – 3.0 GHz	-
Small Signal Gain	14 dB	18 dB	
Gain Ripple	-	± 2.0 dB	± 3.0 dB
P1dB	35.0 dBm	37 dBm	-
Psat	-	38 dBm	-
IP3	-	43 dBm	-
Efficiency @ P1dB	-	30 %	
Input Return Loss	10dB	15dB	
Output Return Loss	6dB	10dB	

Thermal Resistance		5 °C/W	
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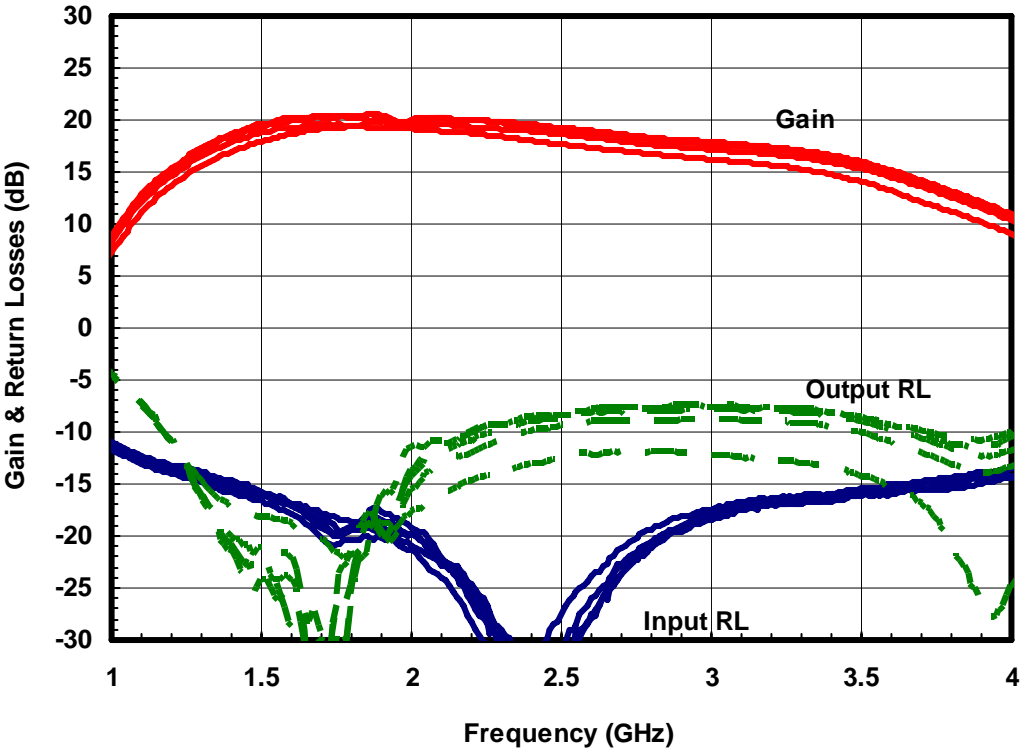
*Specifications subject to change without notice.

**V_{gs} may vary from lot to lot

ABSOLUTE MAXIMUM RATING

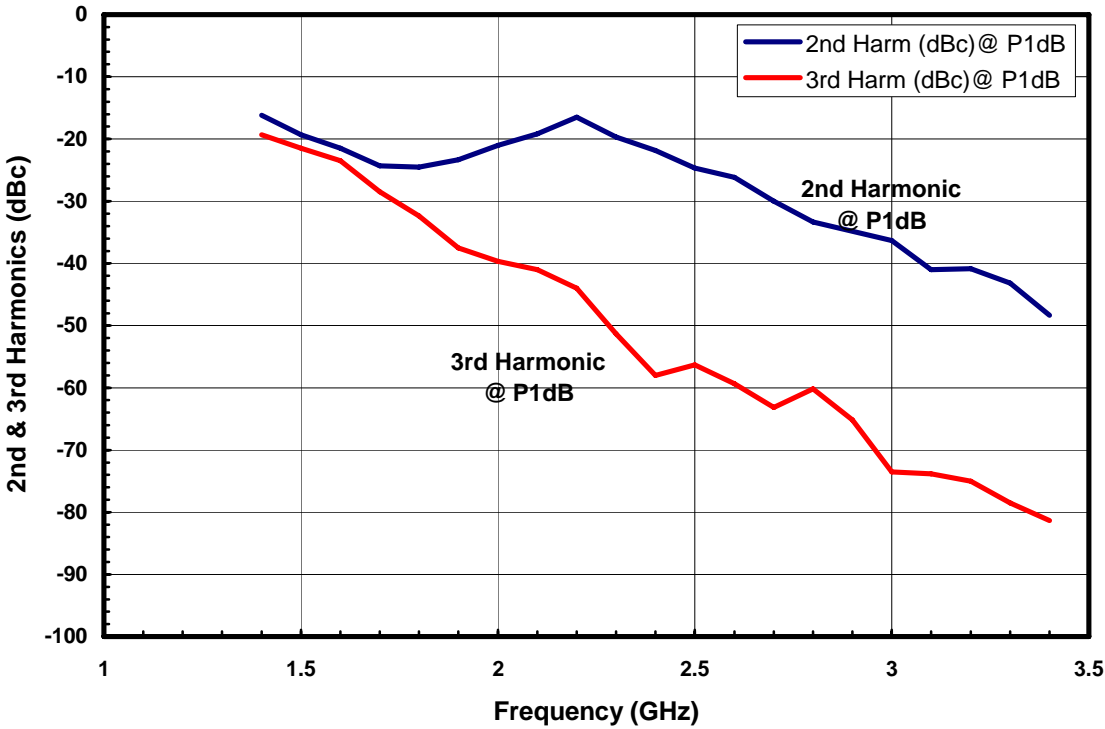
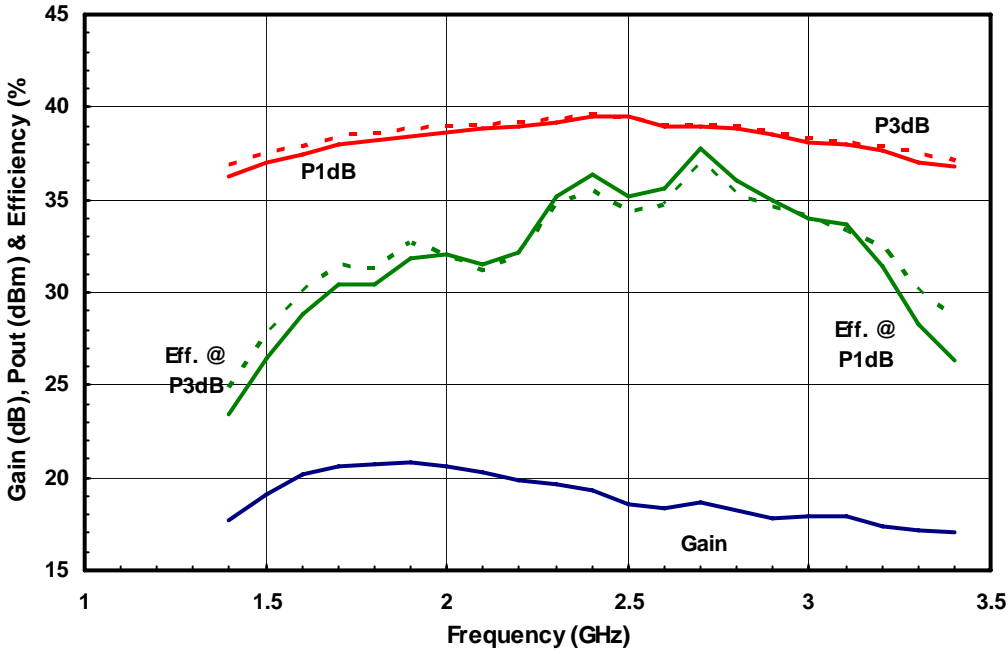
Parameter	Symbol	Rating
Drain source voltage	V _{dd}	13 V
Gate source voltage	V _{gs}	-5 V
Drain source current	I _{dd}	4.0 A
Continuous dissipation at room temperature	P _t	52 W
Channel temperature	T _{ch}	175 °C
Storage temperature	T _{sto}	-55°C to +135°C

SMALL SIGNAL DATA (V_{dd} = +12V, V_{gs}=-0.90V, I_{dq} = 1300mA, T_a = 25°C)**

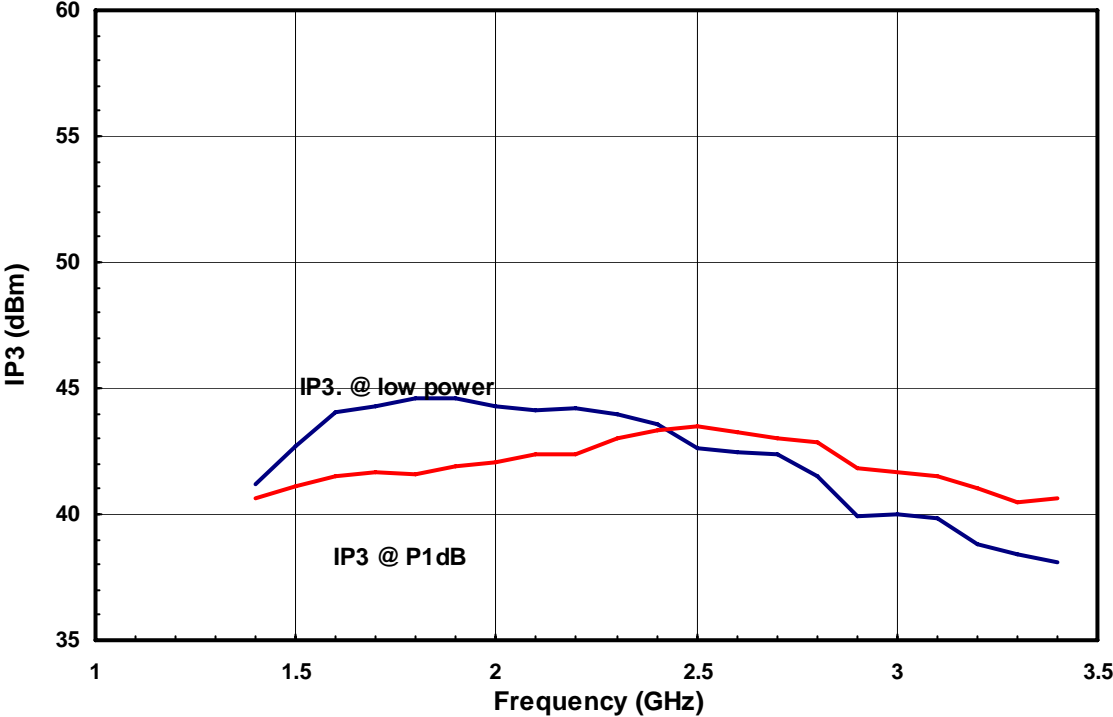


**V_{gs} may vary from lot to lot

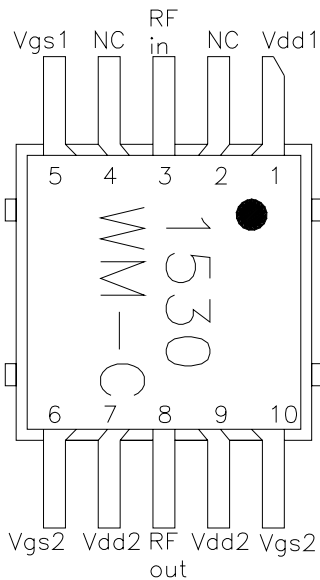
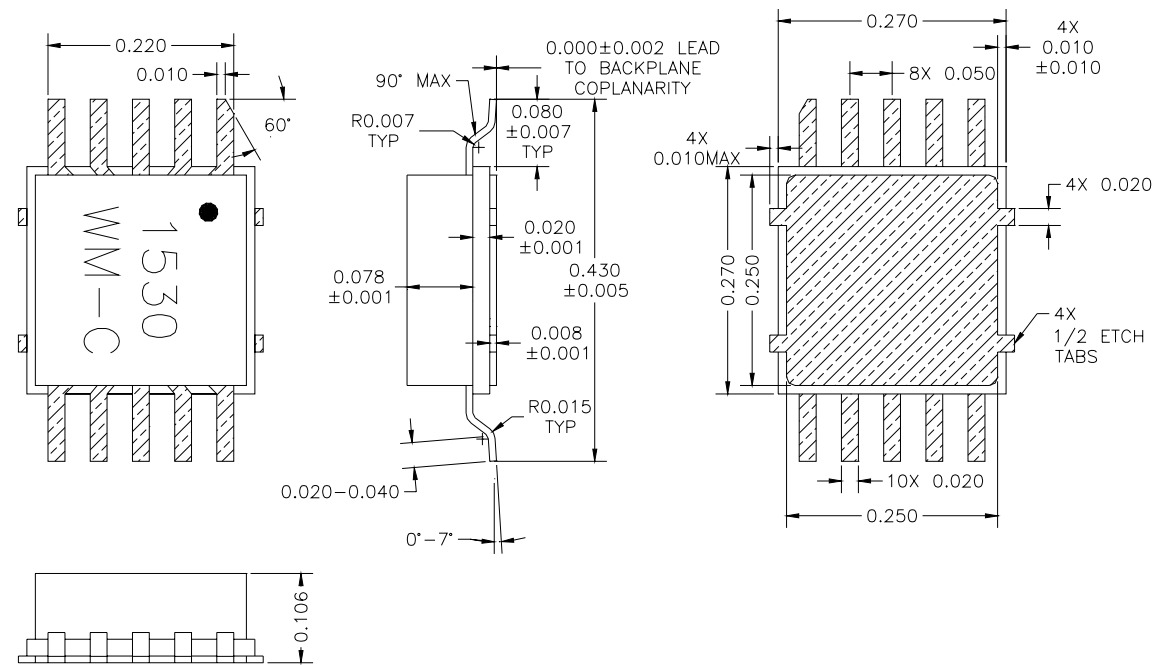
POWER DATA ($V_{dd} = +12V$, $V_{gs} = -0.90V^{**}$, $I_{dq} = 1300mA$, $T_a = 25^\circ C$)



** V_{gs} may vary from lot to lot



PACKAGE OUTLINE (BM)

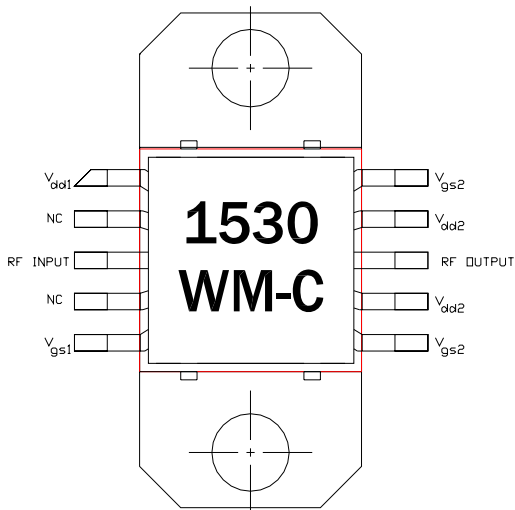
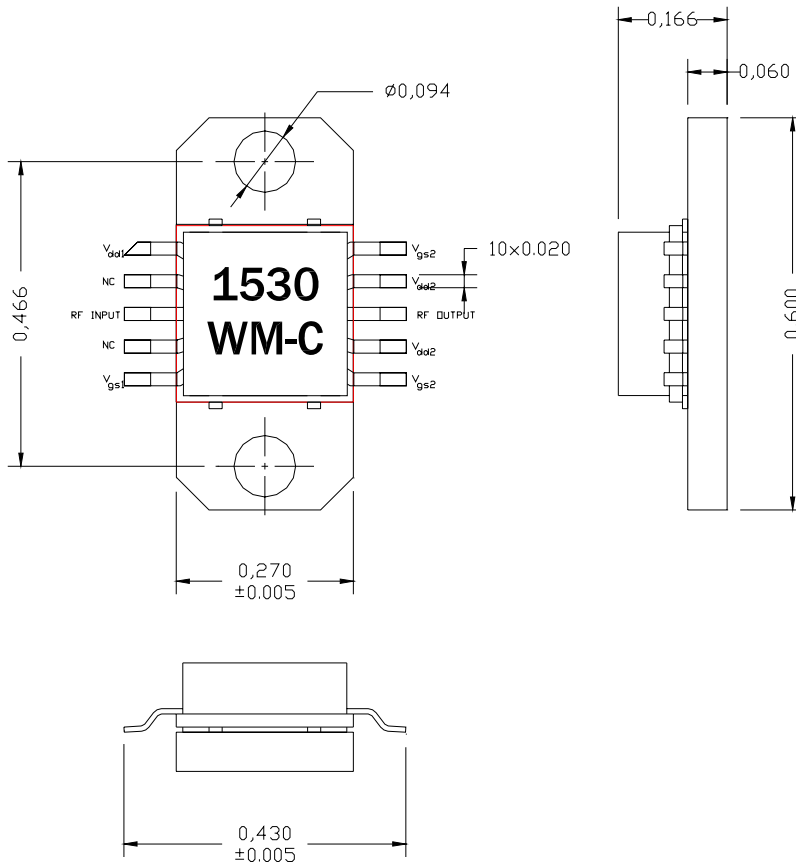


Pin No.	Function	Bias**
1	Vdd1	+12V
2	NC	
3	RF in	
4	NC	
5	Vgs1	-0.9V
6	Vgs2	-0.9V
7	Vdd2	+12V
8	RF out	
9	Vdd2	+12V
10	Vgs2	-0.9V

Pin Layout

** V_{gs1} & V_{gs2} may vary from lot to lot

PACKAGE OUTLINE (FM)

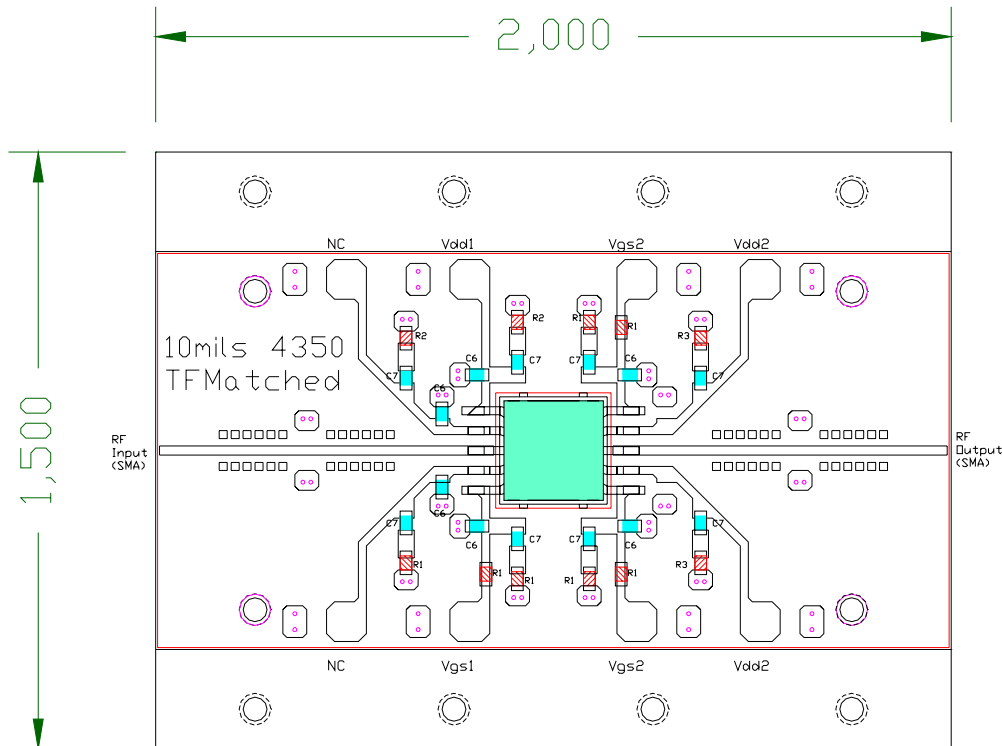


Pin No.	Function	Bias**
1	Vdd1	+12V
2	NC	
3	RF in	
4	NC	
5	Vgs1	-0.9V
6	Vgs2	-0.9V
7	Vdd2	+12V
8	RF out	
9	Vdd2	+12V
10	Vgs2	-0.9V

Pin Layout

** V_{gs1} & V_{gs2} may vary from lot to lot

TEST CIRCUIT for BM package

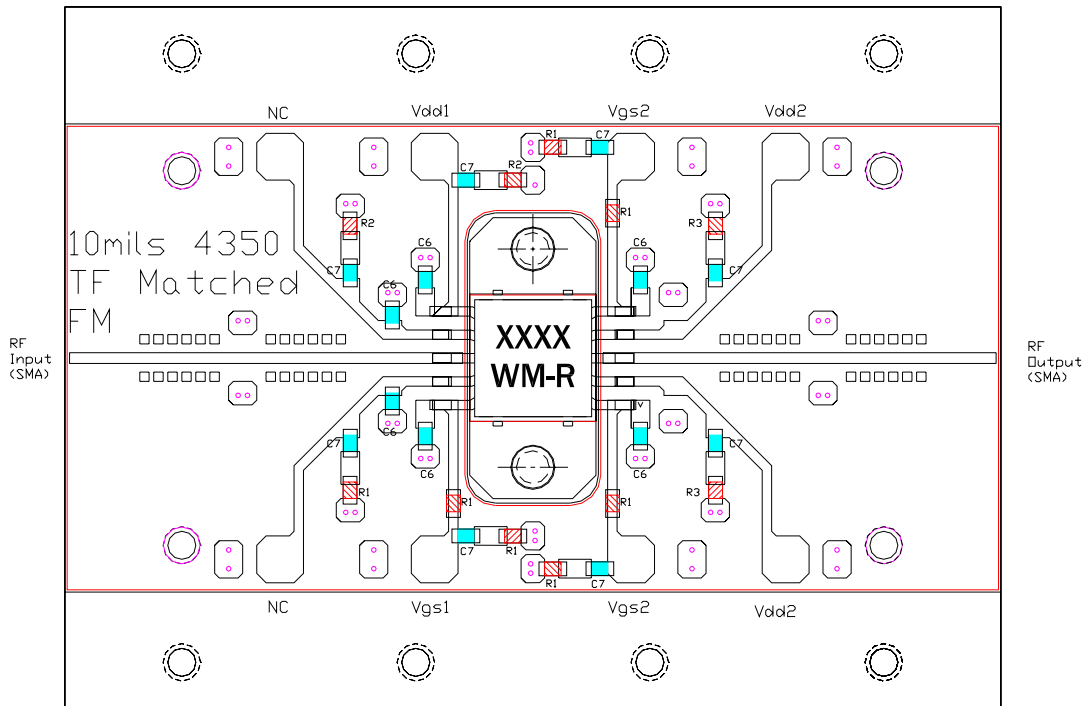


Notes:
 1- 10mils Rogers 4350 Material epoxied
 2- Ckt is for matched MMICs
 3- C6=20pF, C7=1000pF,
 R1=50ohms, R2=10ohms, R3=5ohms
 4- All Caps & Resistors are 0603 size

Important Notes:

- 1- The MMIC should have a good heat sink to avoid overheating. MMIC should be attached on direct ground for lowest junction temperature.
- 2- If surface mount is used, use PC board thickness < 10mils and ensure vias are filled with solder or metal to lower PCB heat resistance. For surface mount the MMC should be de-rated to a maximum +10V bias.
- 3- Recommended current biases are 300mA & 1000mA for the first and second stages respectively.
- 4- Do not apply V_{dd1} & V_{dd2} without proper negative voltages on V_{gs1} & V_{gs2} .
- 5- The currents flowing out of the V_{gs1} & V_{gs2} pins are less than 2mA & 12mA at P_{1dB} .

TEST CIRCUIT for FM package



- Notes:
- 1- 10mils Rogers 4350 Material epoxied
 - 2- Ckt is for matched MMICs
 - 3- C6=20pF, C7=1000pF,
R1=50ohms, R2=10ohms, R3=5ohms
 - 4- All Caps & Resistors are 0603 size